

TM50P03DF
P-Channel Enhancement Mosfet
General Description

- Low $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

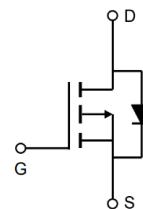
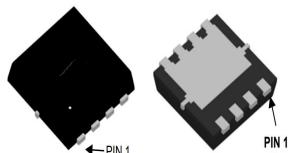
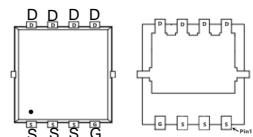
Applications

- Load switch
- PWM

General Features

$V_{DS} = -30V$ $I_D = -50A$
 $R_{DS(ON)} = 9.5m\Omega$ (typ.) @ $V_{GS} = -10V$

100% UIS Tested
 100% R_g Tested


DF:DFN3.3x3.3-8L


Marking: 30P13

Absolute Maximum Ratings ($T_A = 25^\circ C$, unless otherwise noted)

Symbol	Parameter	Rating	Unit	
Common Ratings				
V_{DSS}	Drain-Source Voltage	-30	V	
V_{GSS}	Gate-Source Voltage	± 25		
T_J	Maximum Junction Temperature	150	$^\circ C$	
T_{STG}	Storage Temperature Range	-55 to 150		
I_S	Diode Continuous Forward Current	$T_C = 25^\circ C$	A	
I_D	Continuous Drain Current	$T_C = 25^\circ C$		-50
		$T_C = 100^\circ C$		-32.5
I_{DM}	Pulsed Drain Current	$T_C = 25^\circ C$	W	
P_D	Maximum Power Dissipation	$T_C = 25^\circ C$		32.9
		$T_C = 100^\circ C$		13.2
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	$^\circ C/W$	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	$t \leq 10s$	40	
		Steady State	75	
I_{AS}^a	Avalanche Current, Single pulse	$L = 0.5mH$	A	
E_{AS}^a	Avalanche Energy, Single pulse	$L = 0.5mH$	mJ	

Note * : Current limited by bond wire.

 Note a : UIS tested and pulse width are limited by maximum junction temperature $150^\circ C$
 (initial temperature $T_J = 25^\circ C$).

 Note b : $t < 10s$.

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

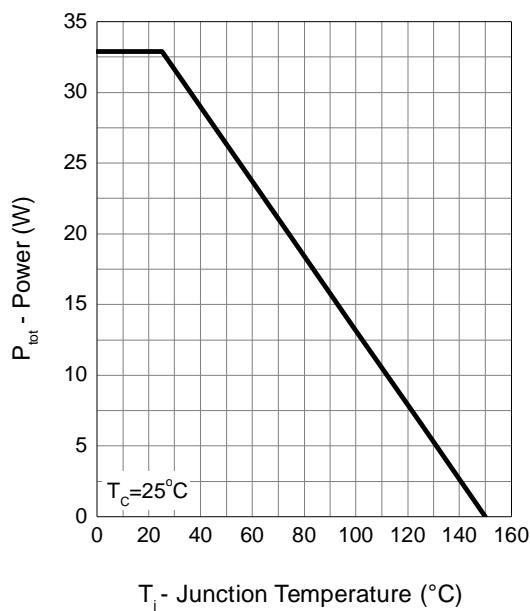
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{DS}}=-250\mu\text{A}$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
		$T_J=85^\circ\text{C}$	-	-	-30	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=-250\mu\text{A}$	-1.3	-1.8	-2.3	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 25\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 10	μA
$R_{\text{DS(ON)}}^{\text{c}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=-10\text{V}, I_{\text{DS}}=-20\text{A}$	-	9.5	13	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{DS}}=-10\text{A}$	-	14	18	
Diode Characteristics						
V_{SD}^{c}	Diode Forward Voltage	$I_{\text{SD}}=-1\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.7	-1	V
t_{rr}^{d}	Reverse Recovery Time	$I_{\text{SD}}=-20\text{A}, dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	20	-	ns
Q_{rr}^{d}	Reverse Recovery Charge		-	8	-	nC
Dynamic Characteristics ^d						
R_g	Gate Resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	9	-	Ω
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-15\text{V}, \text{Frequency}=1.0\text{MHz}$	-	1380	-	pF
C_{oss}	Output Capacitance		-	280	-	
C_{rss}	Reverse Transfer Capacitance		-	217	-	
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{DD}}=-15\text{V}, R_L=15\Omega, I_{\text{DS}}=-1\text{A}, V_{\text{GEN}}=-10\text{V}, R_G=6\Omega$	-	11	-	ns
t_r	Turn-on Rise Time		-	11	-	
$t_{\text{d(OFF)}}$	Turn-off Delay Time		-	101	-	
t_f	Turn-off Fall Time		-	60	-	
Gate Charge Characteristics ^d						
Q_g	Total Gate Charge	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{DS}}=-20\text{A}$	-	30	-	nC
Q_{gs}	Gate-Source Charge		-	1.2	-	
Q_{gd}	Gate-Drain Charge		-	11	-	

 Note c : Pulse test ; pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

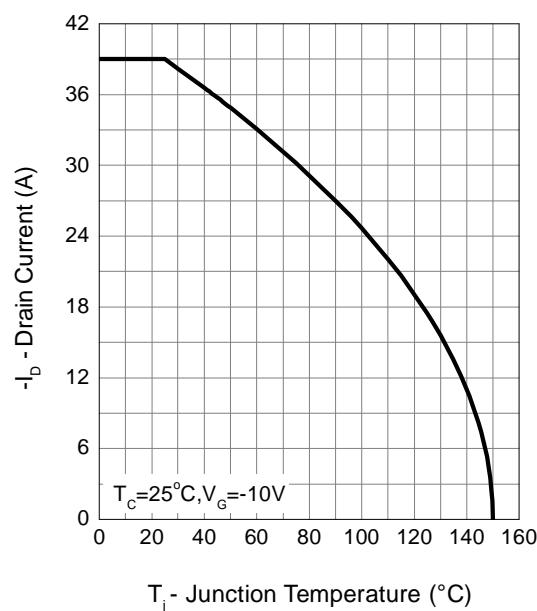
Note d : Guaranteed by design, not subject to production testing.

Typical Performance Characteristics

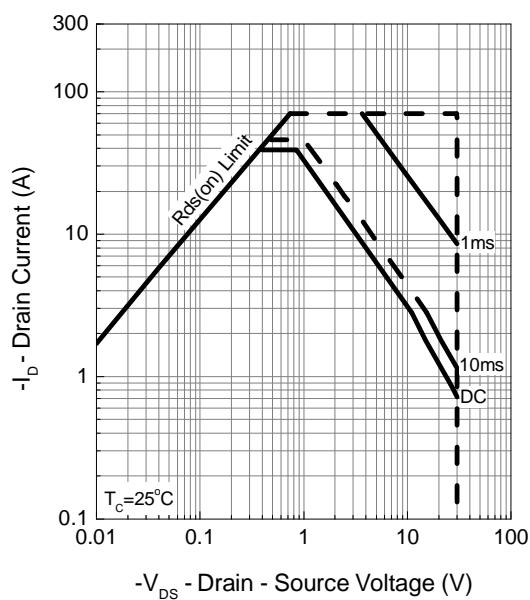
Power Dissipation



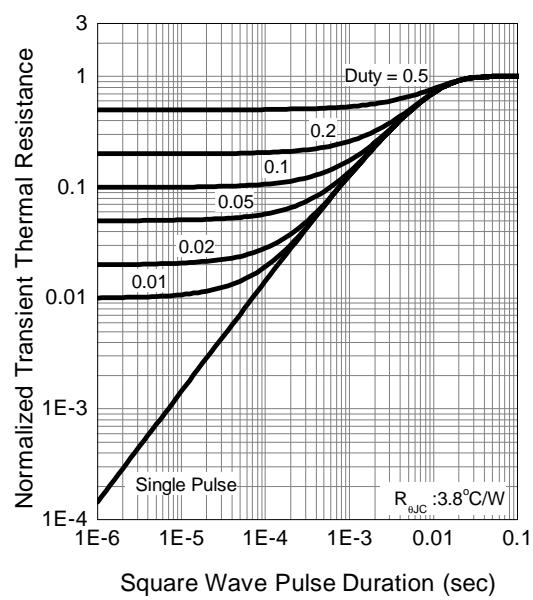
Drain Current



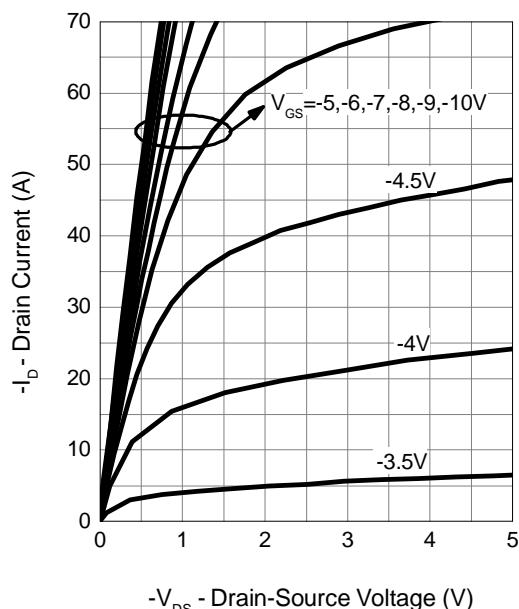
Safe Operation Area



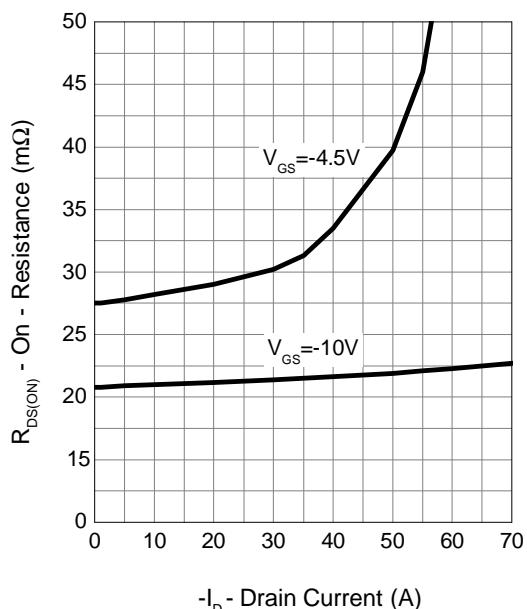
Thermal Transient Impedance



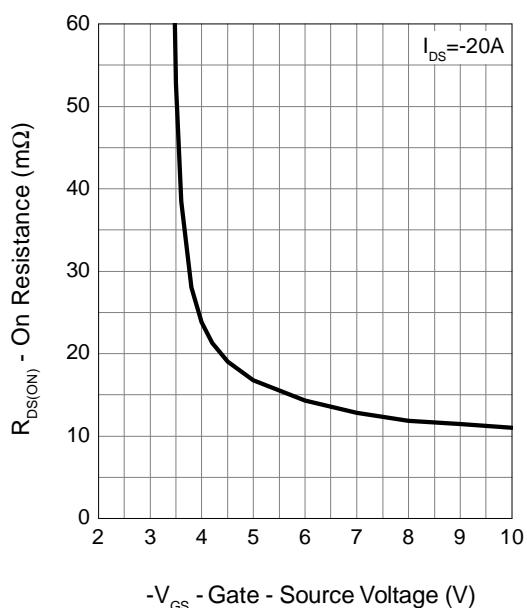
Output Characteristics



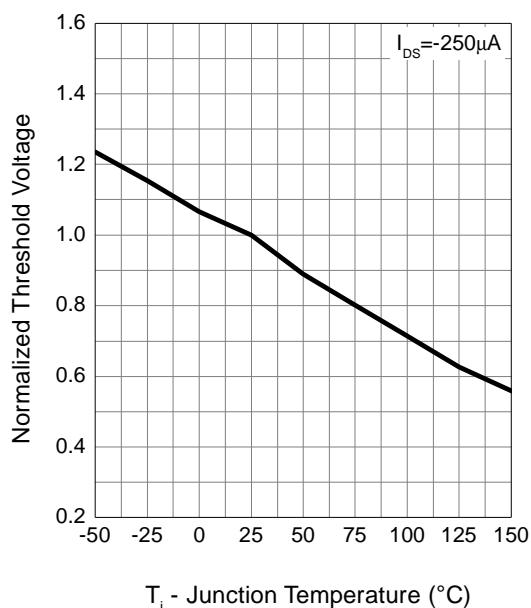
Drain-Source On Resistance



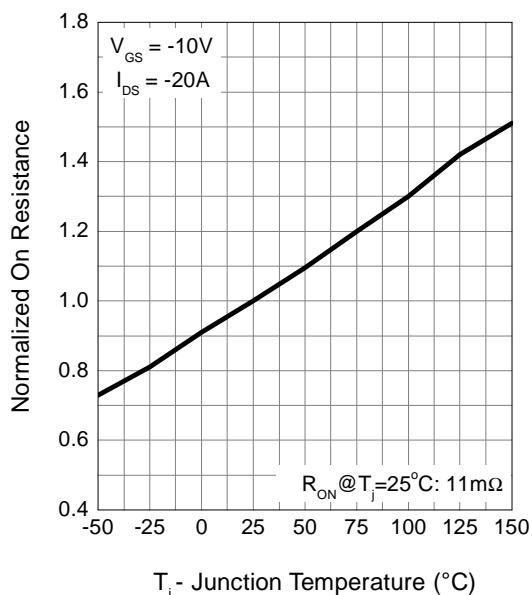
Gate-Source On Resistance



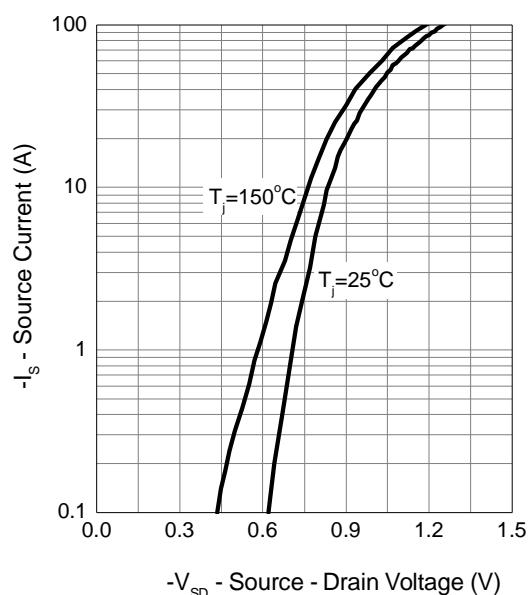
Gate Threshold Voltage



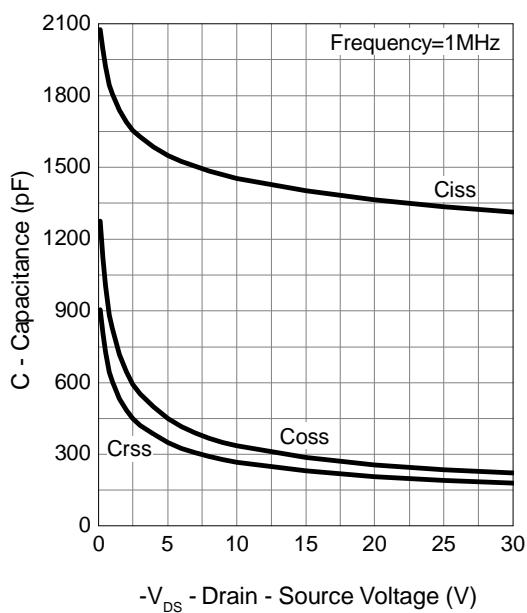
Drain-Source On Resistance



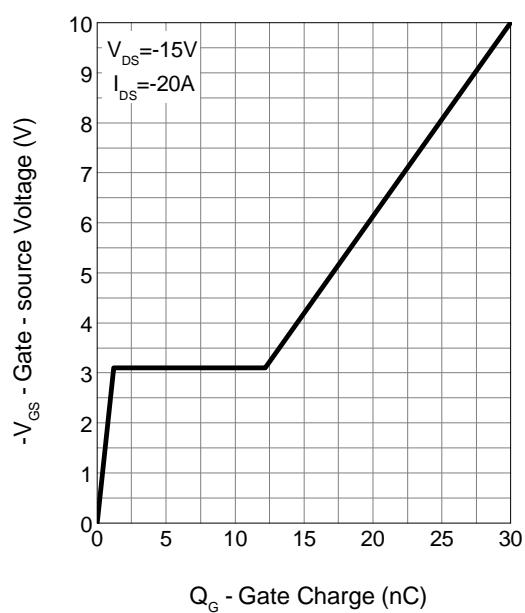
Source-Drain Diode Forward



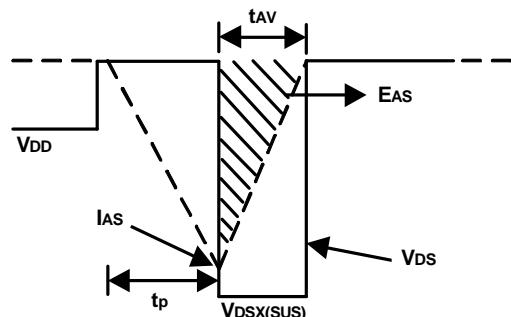
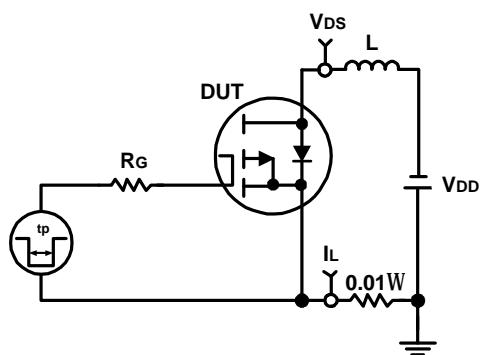
Capacitance



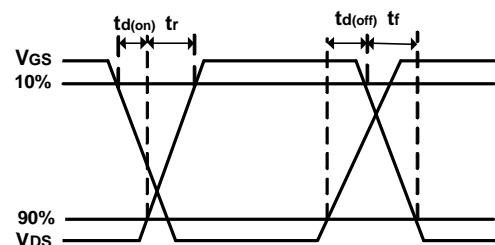
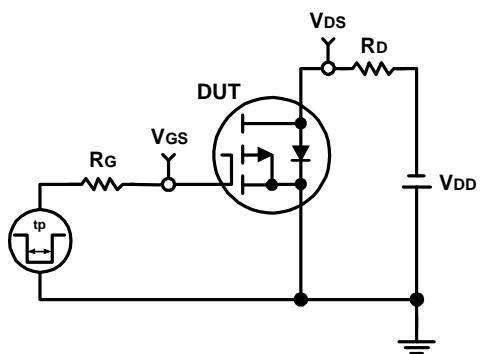
Gate Charge



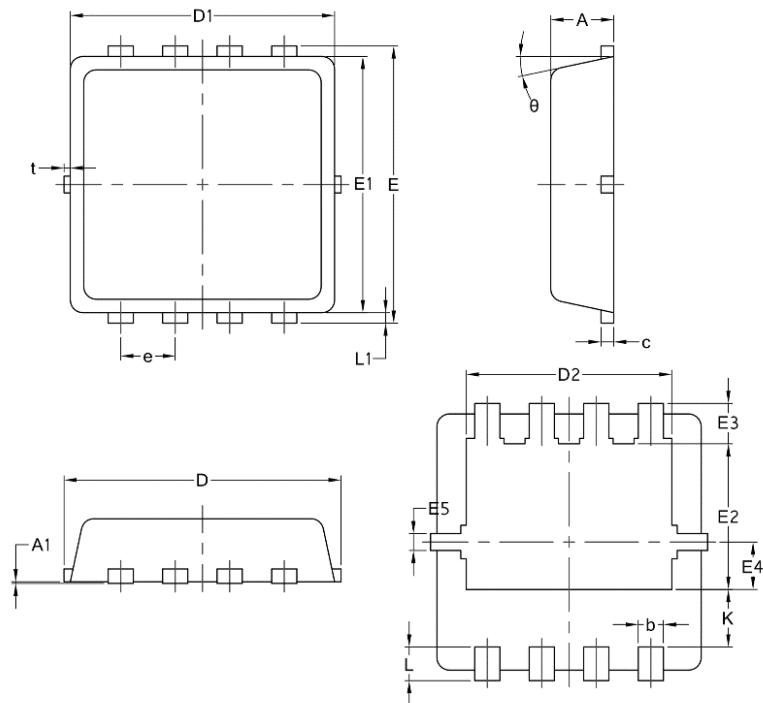
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Package Mechanical Data:DFN3x3-8L



Symbol	Common		
	mm		
	Mim	Nom	Max
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
Φ	10	12	14